

IN THE CLAIMS

Please amend the claims as follows, substituting any amended claim(s) for the corresponding pending claim(s):

- 1 1. (Currently Amended) A method for manufacturing an on-chip inductor comprises:  
2 creating a dielectric layer; and  
3 creating a conductive winding on the dielectric layer, wherein the conductive winding has a  
4 substantially square geometry, wherein corners of the conductive winding are geometrically shaped to  
5 reduce impedance of the on-chip inductor ~~at an operating frequency with~~ when at an operating frequency  
6 and to provide negligible effects on inductance of the on-chip inductor when at the operating frequency.
- 1 2. (Original) The method of claim 1, wherein the creating of the conductive winding further  
2 comprises:  
3 creating the geometric shaping of the corners to include an interior angle per corner of  
4 approximately ninety degrees, and an exterior angle per corner of approximately one hundred thirty-five  
5 degrees.
- 1 3. (Original) The method of claim 1, wherein the creating of the conductive winding further  
2 comprises:  
3 creating the geometric shaping of the corners to include an interior angle per corner of  
4 approximately one hundred thirty-five degrees, and an exterior angle per corner of approximately one  
5 hundred thirty-five degrees.
- 1 4. (Currently Amended) The method of claim 1 further comprises:  
2 creating the conductive winding to have a spiral configuration, wherein the corners of the spiral  
3 configuration are geometrically shaped to reduce impedance of the on-chip inductor ~~at the operating~~  
4 frequency when at the operating frequency.
- 1 5. (Original) The method of claim 1, wherein the creating of the conductive winding further  
2 comprises:  
3 creating a first winding on a first layer; creating a second winding on a second layer; and  
4 connecting the first winding to the second winding with at least one bridge.

1 6. (Currently Amended) The method of claim 1, wherein the creating of the conductive winding  
2 further comprises:

3 creating the geometric shaping of the corners to include angled exterior corners, wherein at least  
4 one angle per exterior corner reduces current turbulence in the corner ~~at the operating frequency~~ when at  
5 the operating frequency.

1 7. (Currently Amended) The on-chip inductor of claim 6, wherein the creating of the conductive  
2 winding further comprises:

3 creating the geometric shaping of the corners to include angled interior corners, wherein at least  
4 one angle per interior corner further reduces current turbulence in the corner ~~at the operating frequency~~  
5 when at the operating frequency.

Claims 8-15. (Cancelled)